

CBL-104/DIV

## IN THE US PATENT AND TRADEMARK OFFICE

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*#5/B*  
*7-10-03*

Application Number: 10/023,910  
Agent's Docket Number: CBL-104/DIV  
5 Filing Date: December 18, 2001  
Applicant: Glenn S. Solomon et al.  
Application Title: DUAL PROCESS SEMICONDUCTOR HETEROSTRUCTURES  
Examiner: Victor Yevsikov  
10 Art Unit: 2825

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DATE OF TRANSMISSION: JUNE 23, 2003

*Joshua D. Iseneberg*  
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Joshua D. Iseneberg, Reg. No 41088

NAME OF PERSON SIGNING

AMENDMENT B  
(PRELIMINARY)

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Commissioner of Patents  
15 Alexandria, VA 22313-1450

JUN 23 2003

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Sir:

Prior to examination of the above application, kindly amend the application as follows.

AMENDMENTS TO THE CLAIMS begin on page 2 of this paper. All pending claims are shown.

20 REMARKS begin on page 4 of this paper.

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**JDI PATENT**

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To: Examiner Victor Yevsikov From: Joshua D. Isenberg Ph.D.  
Fax: (703) - 746-4117 Pages: 5 (including cover sheet)  
Phone: (703) - 305-0758 Date: June 23, 2003  
Re: Appl. No: 10/023,910 (CBL-104/017) CC:  
☐ Urgent ☒ For Review ☐ Please Comment ☐ Please Reply ☐ Please Recycle

**• Comments:**

Examiner Yevsikov:

Enclosed is the preliminary amendment for  
application 10/023,910 that we discussed.

Please confirm receipt by email to  
josh@jdi-patent or fax to  
(510)-360-9656

Sincerely,

*Joshua D. Isenberg*

Joshua D. Isenberg  
Reg. No. 41088

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